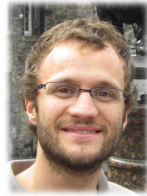


Simon Pontié

Curriculum Vitae



Contacts



École des Mines de Saint-Étienne
Campus Georges Charpak Provence
880, route de Mimet
13541 GARDANNE Cedex France
+33442616728



simon.pontie@cea.fr
<https://www.simon.pontie.fr>

WORK EXPERIENCES

- Current** **Researcher** at CEA Cadarache, [CEA Tech across France](#), Provence-Alpes-Côte d'Azur region
[SAS](#) research group (*Secure Architectures and Systems*), Center of Microelectronics in Provence
- 2013-2016** **Ph.D.**, Grenoble-Alpes University (*France*), TIMA Laboratory, [AMfoRS](#) research group
Ph.D. thesis defence at Grenoble: November 21st, 2016. Jury: Arnaud Tisserand, Lionel Torres, Philippe Elbaz-Vincent, Pierre-Yvan Liardet, Viktor Fischer, Régis Leveugle, and Paolo Maistri
3 years of research, supervisors: Régis Leveugle and Paolo Maistri
Hardware security for cryptography based on elliptic curves ([link](#))
3 years as a teacher at Phelma (*school of Physics and Electronics in Grenoble*) in addition to my PhD studies
digital and analog electronics, real-time systems, security of embedded systems, embedded systems
- February-June 2012** Electronic sciences research during Master internship: TIMA Laboratory, Grenoble
Design and validation of a secure crypto-processor for cryptography based on elliptic curves
- June-July 2010** Electronic sciences research during Bachelor internship: BIOMIS Laboratory, Rennes, France
Design of a potentiostat for micro sensor

EDUCATION

- 2013-2016** **Ph.D.**, Grenoble-Alpes University, TIMA Laboratory, Grenoble, France
Three years, researcher and teacher as Ph.D. student
- 2012-2013** **Research Master**: Joseph Fourier University, Grenoble
Nanoelectronic and Nanotechnology, Hardware design
- June 2012** National Competitive Examination for Teacher Training in Electrical Engineering (*French agrégation*)
- 2011-2012** **Master**: École Normale Supérieure de Cachan, Rennes
Higher-education teacher training in electrical engineering
Preparation of the *French agrégation* in electrical engineering
- 2010-2011** First year of **Master**: École Normale Supérieure de Cachan and University of Rennes
Electronics and Telecommunications
First year of **Master**: École Normale Supérieure de Cachan and University of Rennes
Mechanical engineering
- 2009-2010** **Bachelor**: École Normale Supérieure de Cachan and University of Rennes
Electronics and Telecommunications
Bachelor: École Normale Supérieure de Cachan and University of Rennes
Mechanical engineering

SPECIAL SKILLS

- Hardware Design: Modelsim, ISE, EDK, Vivado, HLS softwares, HDLs: vhdl and verilog
- Programming Languages: C, Bash, Matlab, C++, Ruby
- Office automation tools: Latex, Beamer, Word, Power Point
- UNIX system administration

LANGUAGES

- Advanced English level
- Native French speaker
- Basic Spanish communication skills

MISCELLANEOUS

- Ski touring
- Mountain hike
- Personal projects combining electronic and computer sciences

